

Sub B2
4. (Once Amended) A LSI layout method for a LSI design by automatic arrangement wiring of standard cells, wherein logic gate cells and power supply capacitor cells are provided as the standard cells, comprising the operations of:

connecting the power supply line of the logic gate with the ground line of the logic gate through the power supply capacitor cells, and
arranging the power supply capacitor cells in spaces of each block where standard cells are not arranged by the automatic arrangement wiring.

In The Figures

Please replace the current Figure 9 with the attached Figure 9. The only difference is that the revised version has the legend "Prior Art" at the bottom of the Figure.